ARDUINO

ARDUINO
MKR VIDOR 4000

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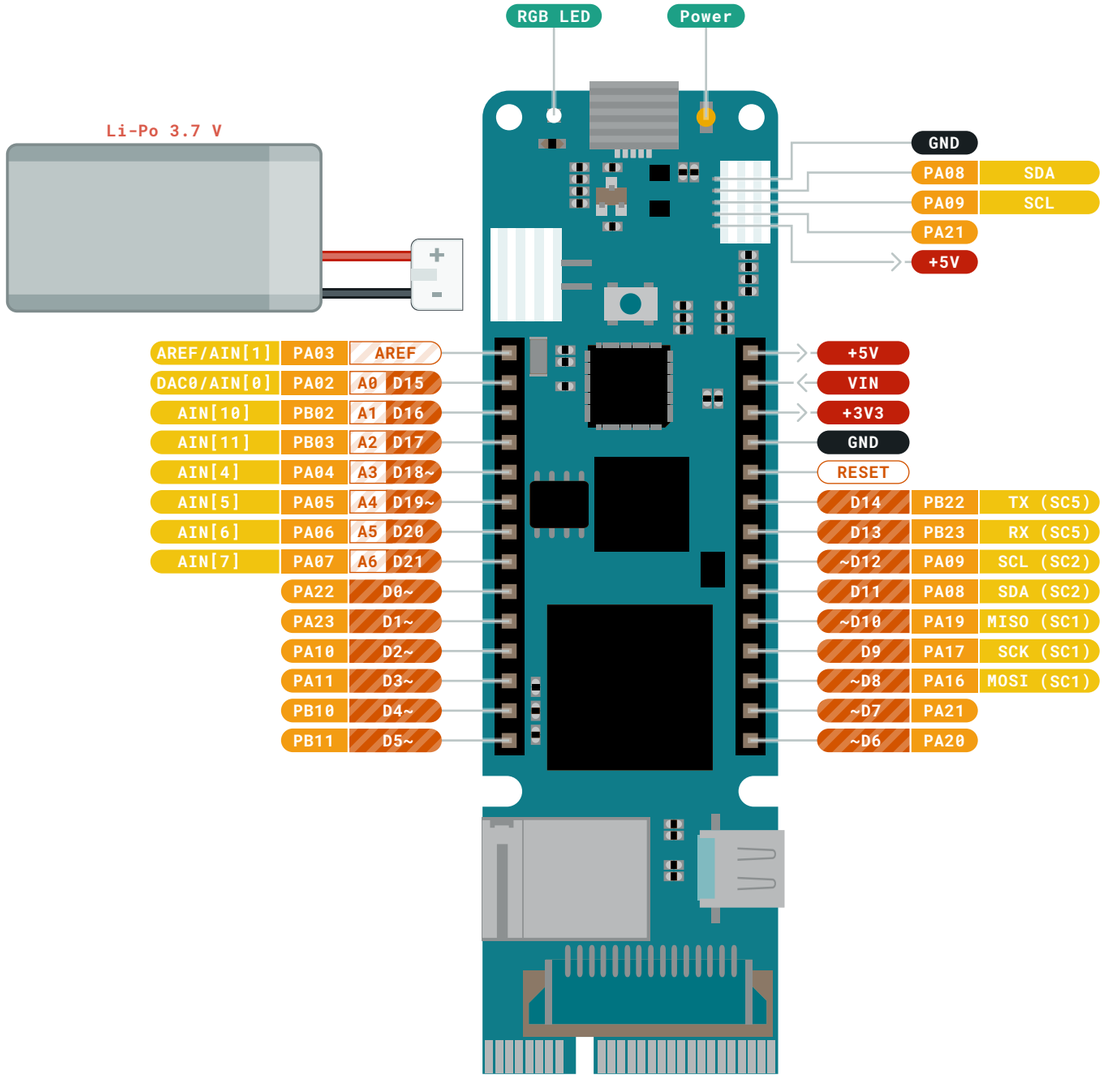

- Ground
- Power
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- Internal Pin
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- Default
- FPGA Port

If programmed with SAMD

- **MAXIMUM** current per pin is 7mA
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If programmed with FPGA

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VIN Input voltage to the board.

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Last update: 4/08/2020

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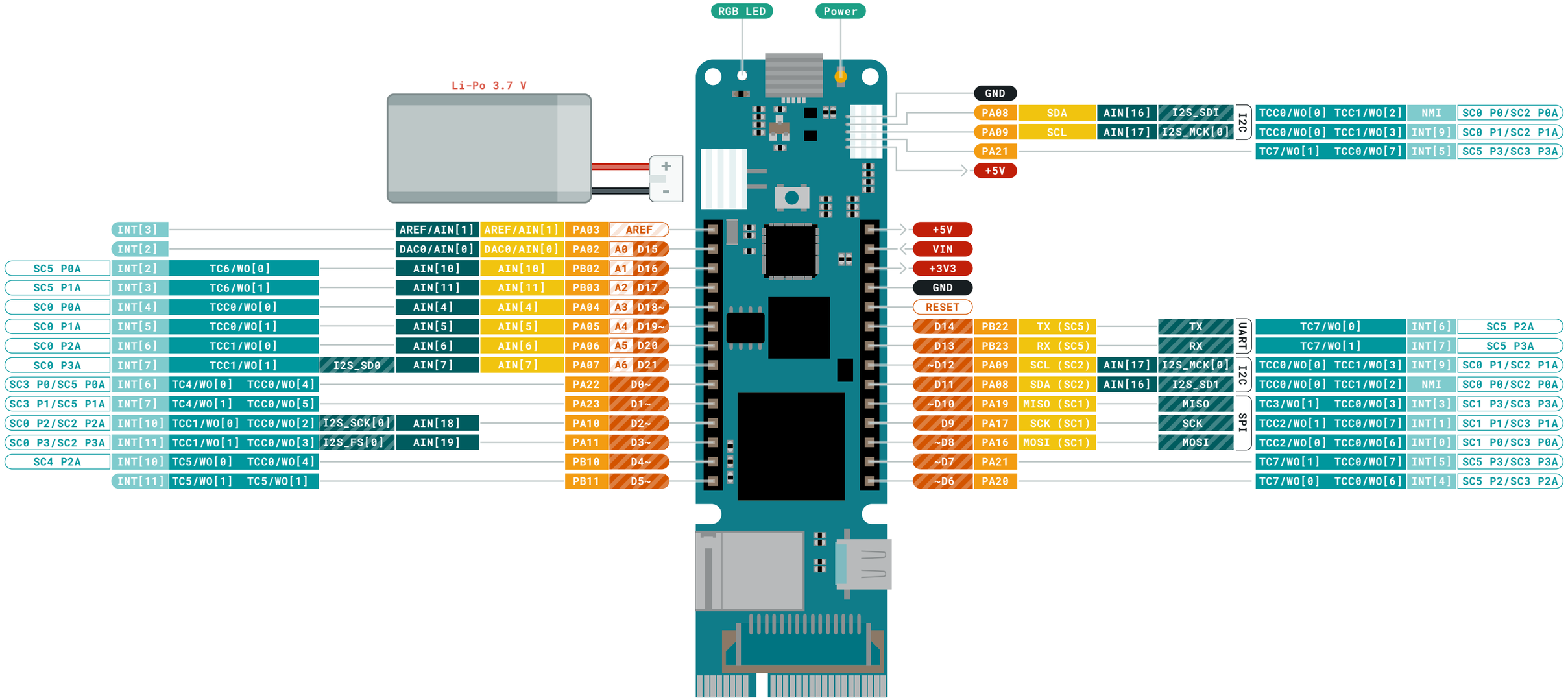

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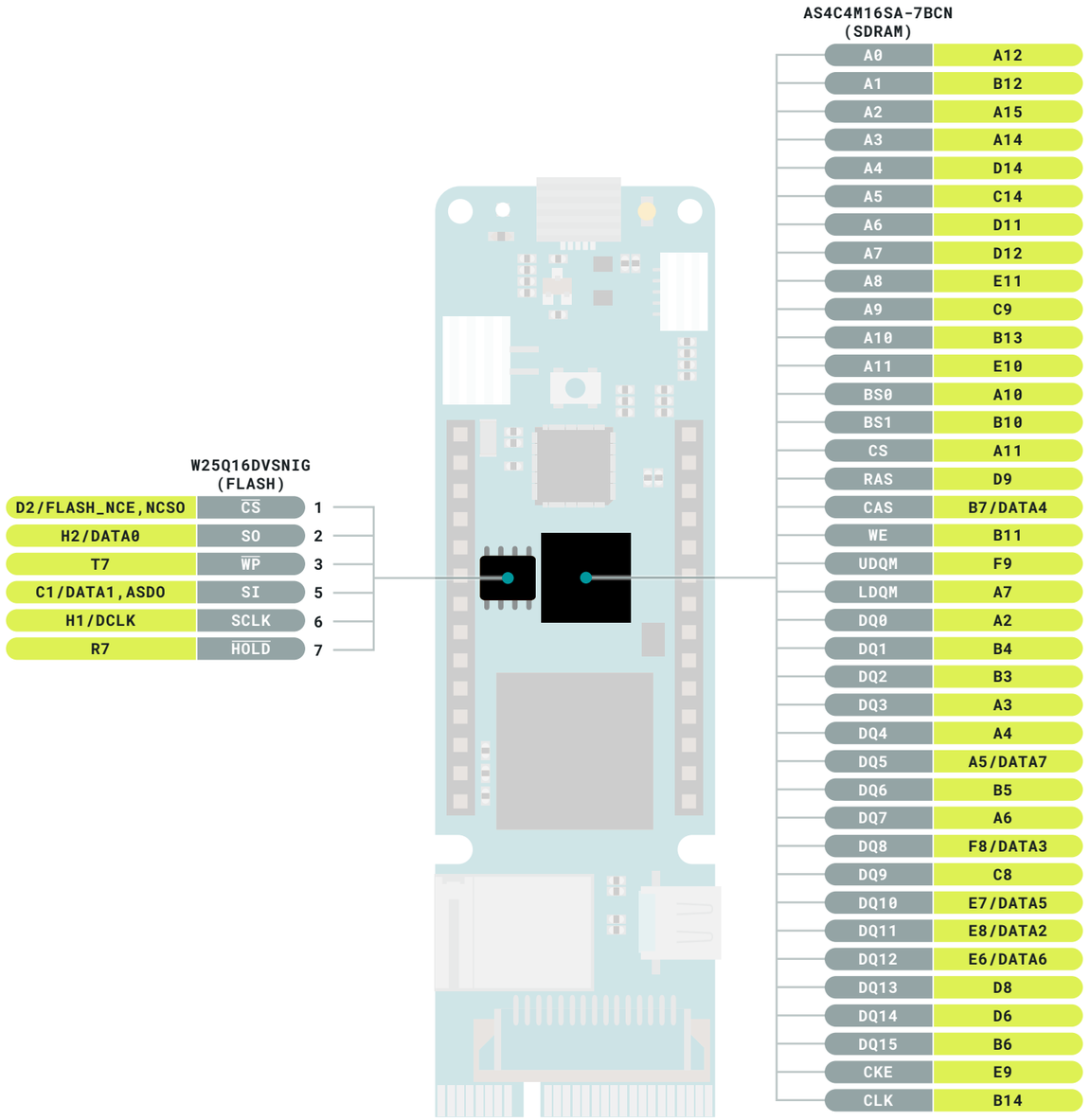

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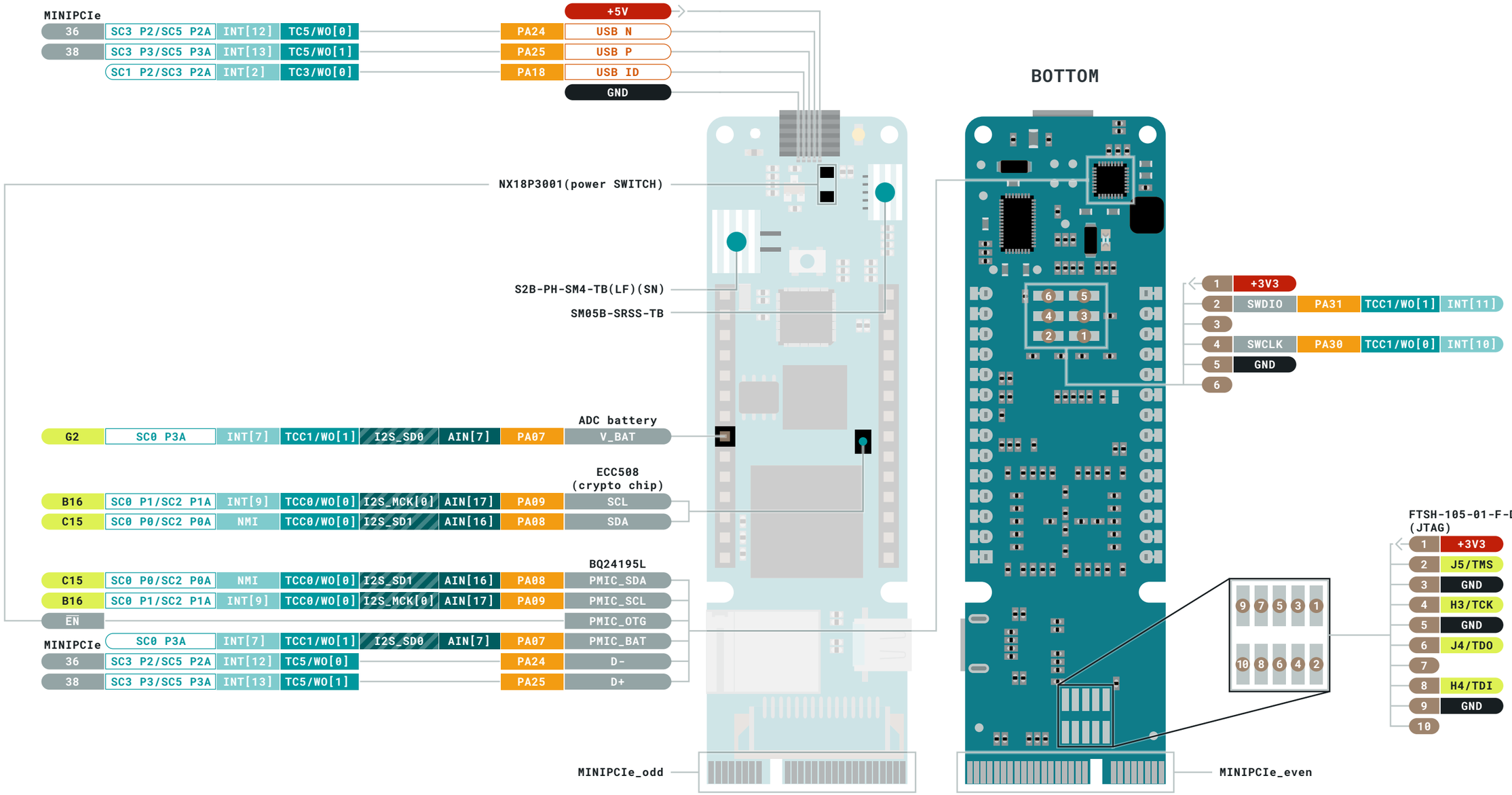

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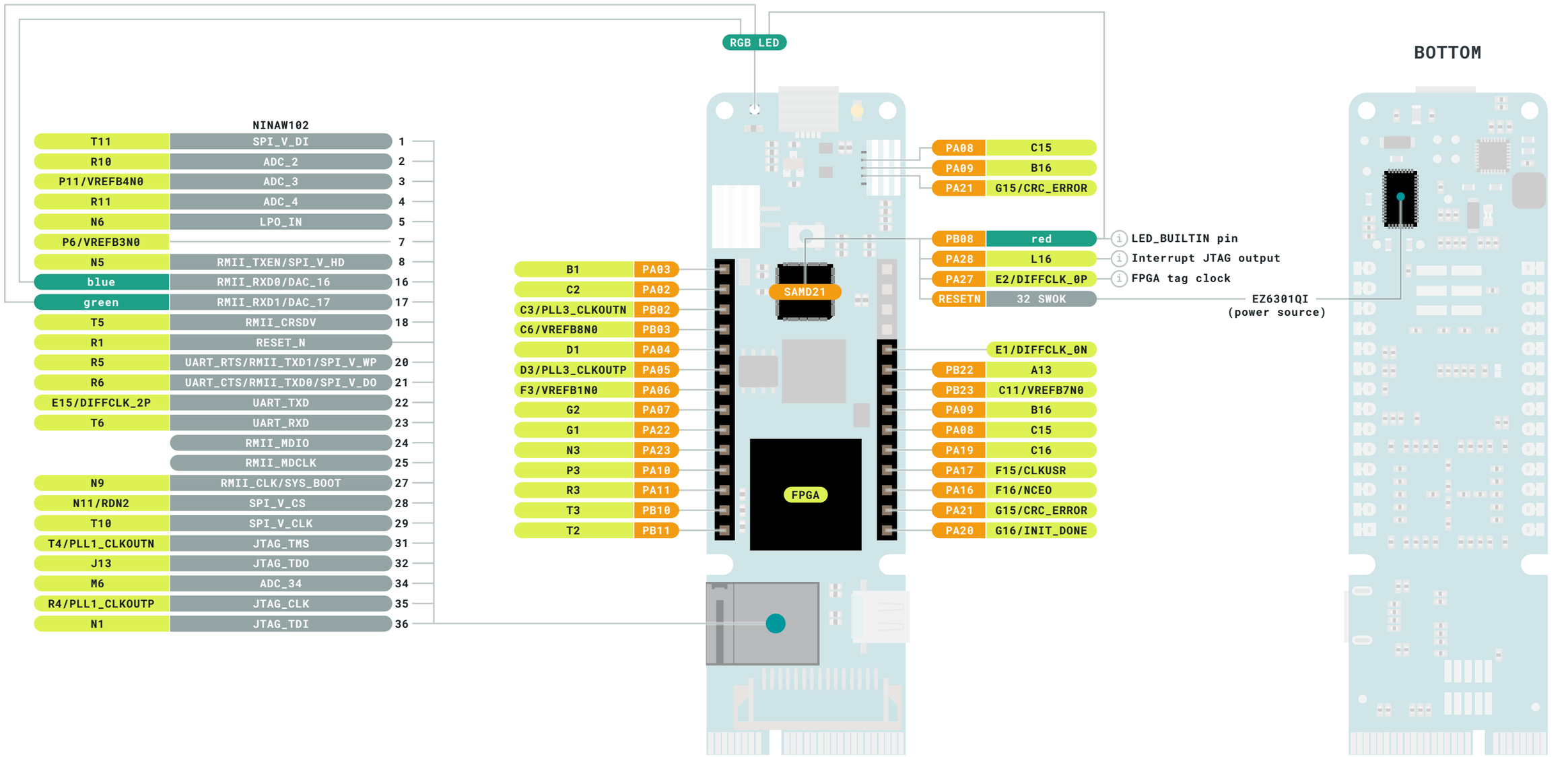

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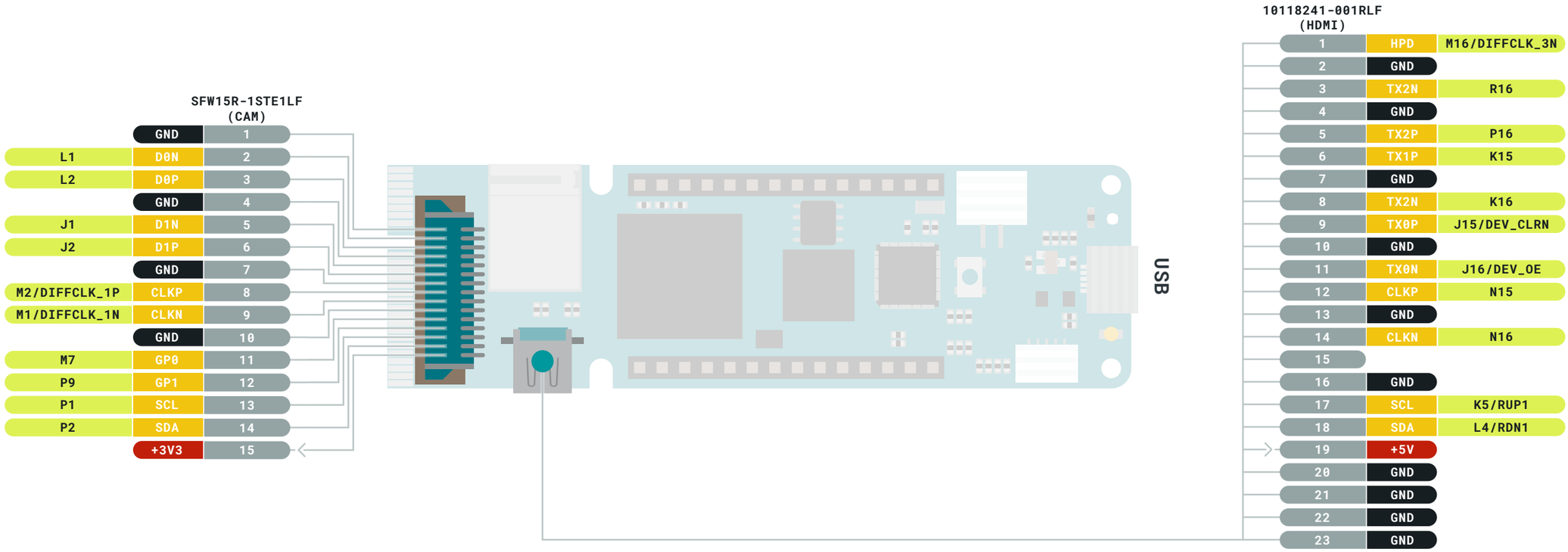

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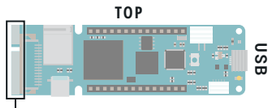
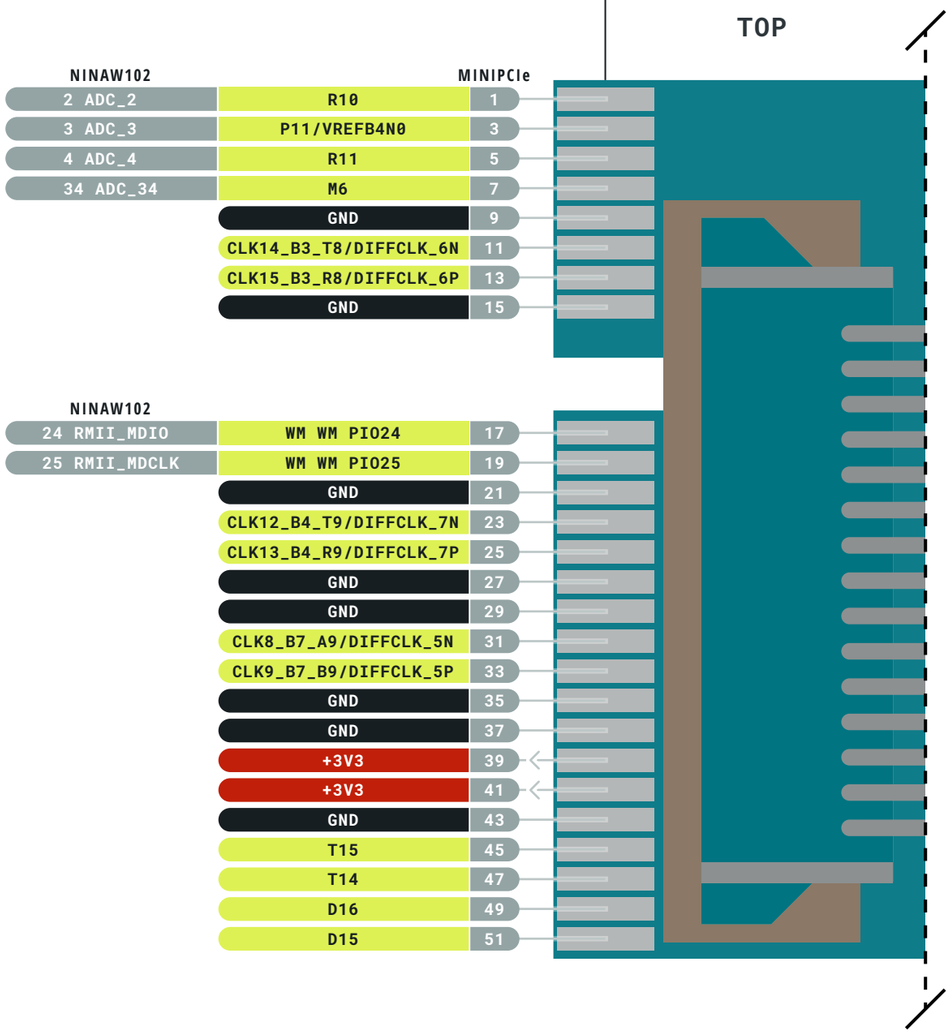
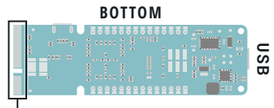
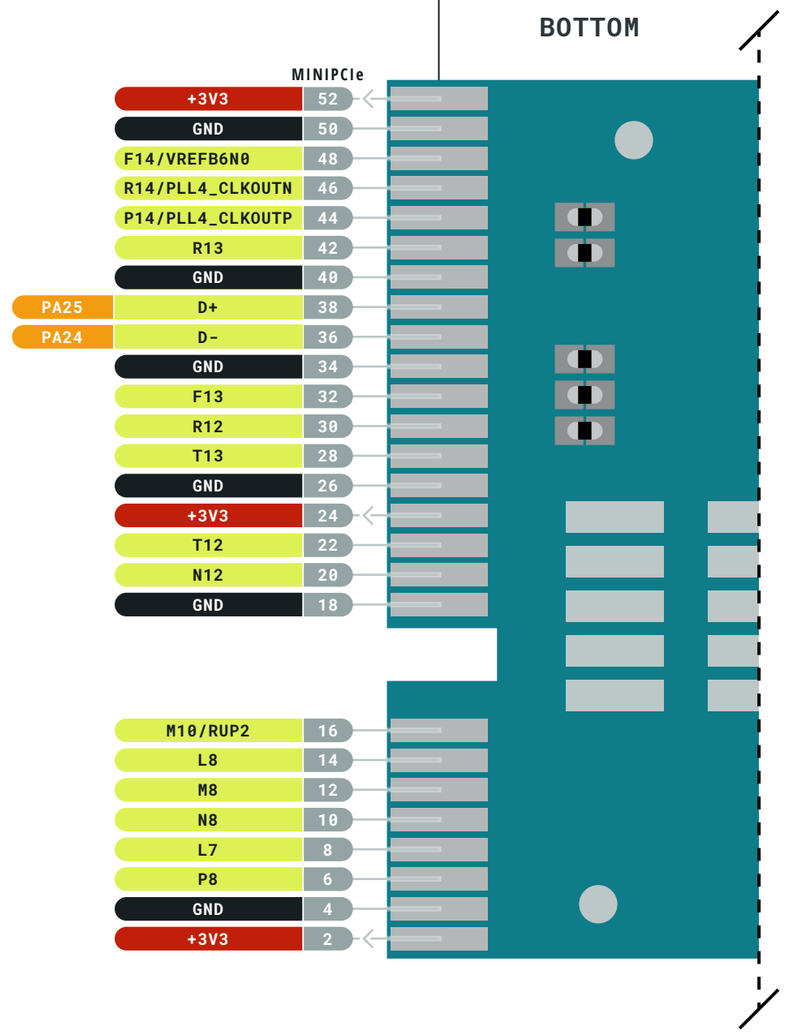
MINIPCIe_odd





MINIPCIe_even





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